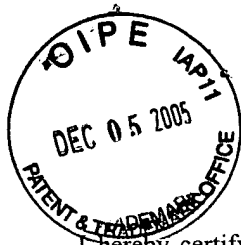


CofC



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 28, 2005
Date

Carolyn L. Ross
Carolyn L. Ross

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Tim Murphy and Lee Gotcher Attorney Docket No.: 501039.03
Patent No. : US 6,936,489 B2 Serial No. : 10/651,594
Issue Date : August 30, 2005 Filed : August 29, 2003
Title : METHOD AND SYSTEM FOR ELECTRICALLY COUPLING A CHIP TO CHIP PACKAGE

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**Certificate
DEC 08 2005
of Correction**

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 3, Line 67	"of the chip 402, the infrared signals 406 propagate though"	--of the chip 402, the infrared signals 406 propagate through--

DEC 09 2005

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date:

Nov. 23, 2005

By:

Edward W. Bulchis

Edward W. Bulchis, Reg. No. 26,847

Customer No. 27,076

Dorsey & Whitney LLP

1420 Fifth Avenue, Suite 3400

Seattle, WA 98101

(206) 903-8785

Attorney for Applicant(s)

EWB:clr

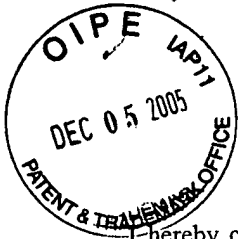
Enclosures:

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Form PTO-1050 (+ copy)

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INVENTOR(S) : Tim Murphy and Lee Gotcher

It is certified that error appears in the above identified patent and that said Letters Patent is hereby corrected as shown below:

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	"	----

MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
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Patent No. US 6,936,489 B2

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**UNITED STATES PATENT AND TRADEMARK OFFICE
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